

**Application Data Sheet**

**Application Information**

|                                  |  |
|----------------------------------|--|
| Application Type::               | Regular  |
| Subject Matter::                 | Utility  |
| Title::                          | Device and Method for Equalising the Charge of Serially Connected Capacitors Belonging to a Double Layer Capacitor |
| Attorney Docket Number::         | 2003P12591   |
| Request for Early Publication::  | No   |
| Request for Non-Publication::    | No   |
| Suggested Drawing Figure::       |  |
| Total Drawing Sheets::           | 4  |
| Small Entity::                   | No   |
| Petition included::              | No   |
| Secrecy Order in Patent Appl.?:: | No   |

**Applicant Information**

|   |                 |
|---|-----------------|
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### Domestic Priority Information

| Application:     | Continuity Type:: | Parent Application:: | Parent Filing Date:: |
|------------------|-------------------|----------------------|----------------------|
| This application | National Stage of | PCT/EP2005/050050    | 01/07/05             |

### Foreign Priority Information

| Country:: | Application number:: | Filing Date:: | Priority Claimed:: |
|-----------|----------------------|---------------|--------------------|
| DE        | 10 2004 005 136.4    | 02/02/04      | Yes                |

### Assignee Information

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